JC564 U.S. PTO
09/538670
03/30/00

712 2C/ Subclass
712 Class ISSUE CLASSIFICATION

OS-0443

U.S. UTILITY Patent Application

	O.I.P.E.	PATENT DATE
SCANNED		

PATENT NUMBER

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/538670		712	2//	2782 2154	Donoghue

APPLICANTS

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TITLE

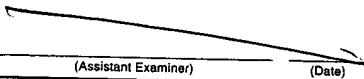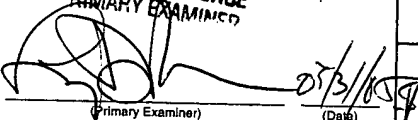Method and apparatus for allocating functional units in a
multithreaded vliw processor

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL			CROSS REFERENCE(S)							
CLASS	SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
712	24		718	103	102					
INTERNATIONAL CLASSIFICATION										
G06F 1T/00										
G06F 9/46										
							☐ Continued on Issue Slip Inside File Jacket			

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
	5	9	7B	16	1

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

(Assistant Examiner) _____ (Date)

☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____

LARRY D. DONAGHUE
PRIMARY EXAMINER
(Primary Examiner) 05/31/05 (Date)

☐ The terminal _____ months of this patent have been disclaimed.

(Legal Instruments Examiner) _____ (Date)

NOTICE OF ALLOWANCE MAILED

1/8/05

ISSUE FEE

Amount Due	Date Paid
$1400.00	

ISSUE BATCH NUMBER

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